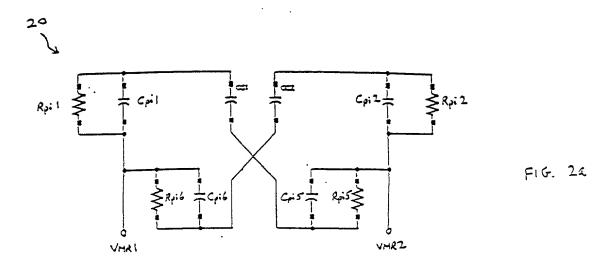
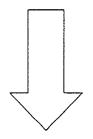


FIG. 1



Equivalent Input Circuit



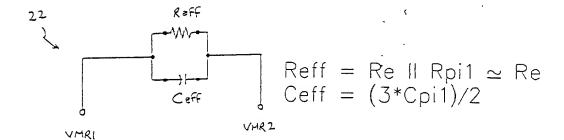
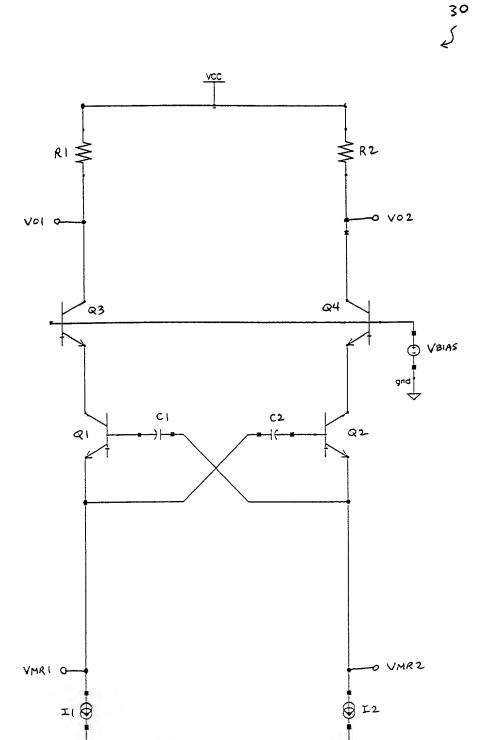


FIG. 26



F16. 3

VEE

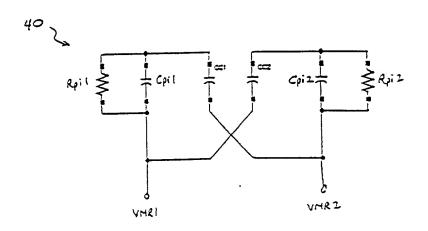
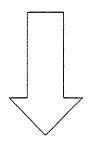
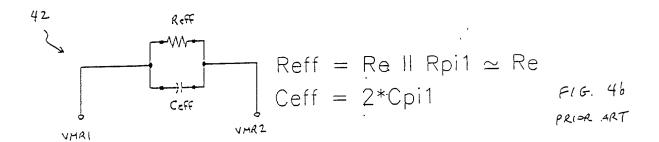


FIG. 4a PRIOR ART

Equivalent Input Circuit





Simplified Circuit

